a control-program-development supporting apparatus that develops a control program described with a sequential-control language such as a ladder diagram or instruction list, ~~which~~ the control-program-development supporting apparatus having a compiler which compiles the control program into codes directly executable by a universal microprocessor that ~~mounts~~ includes an acceleration ~~mounting~~ unit, such as ~~a~~ pipeline logic and a cache.

Amendments to the abstract:

ABSTRACT OF THE DISCLOSURE

~~The~~ A programmable controller ~~mounts an~~ includes a universal microprocessor. This universal microprocessor ~~comprises~~ incorporates an acceleration ~~mounting art~~ section. The control-program-development supporting apparatus generates an execution code directly executable by the universal microprocessor. Thereby, the acceleration ~~mounting art~~ section executes sequential processing.